1MBI900VXA-120PC-50

IGBT Modules

IGBT MODULE (V series)
1200V / 900A / 1 in one package

Features

High speed switching
Voltage drive
Low Inductance module structure

Applications

NPC 3-level Inverter
Inverter DB for Motor Drive
AC and DC Servo Drive Amplifier (DB)
Active PFC
Industrial machines

Maximum Ratings and Characteristics

● Maximum Ratings (at T_c=25°C unless otherwise specified)

Items		Symbols	Conditions		Maximum ratings	Units
Collector-Emitter voltage		V_{CES}			1200	V
Gate-Emitter voltage		V_{GES}			±20	V
Collector current for IGBT and Inverse Diode		I_C	Continuous	T_c=25°C	1200	A
				T_c=100°C	900	
		$I_{C\ pulse}$	1ms		1800	
		$-I_C$			120	
		$-I_{C\ pulse}$	1ms		240	
Collector Power Dissipation		P_C	1 device		5100	W
Reverse voltage for FWD		V_R			1200	V
Forward current for FWD		I_F	Continuous		900	A
		$I_{F\ pulse}$	1ms		1800	
Junction temperature		T_j			175	°C
Operating junction temperature (under switching conditions)		T_{jop}			150	
Case temperature		T_C			150	
Storage temperature		T_{stg}			-40 ~ +150	
Isolation voltage	between terminal and copper base (*1)	V_{iso}	AC : 1min.		4000	VAC
	between thermistor and others (*2)					
Screw Torque (*3)	Mounting	-	M5		6.0	N m
	Main Terminals	-	M8		10.0	
	Sense Terminals	-	M4		2.1	

Note *1: All terminals should be connected together during the test.
Note *2: Two thermistor terminals should be connected together, other terminals should be connected together and shorted to base plate during the test.
Note *3: Recommendable Value : Mounting 3.0 ~ 6.0 Nm (M5)
Recommendable Value : Main Terminals 8.0 ~ 10.0 Nm (M8)
Recommendable Value : Sense Terminals 1.8 ~ 2.1 Nm (M4)

● Electrical characteristics (at T_j= 25°C unless otherwise specified)

Items		Symbols	Conditions		Characteristics min.	Characteristics typ.	Characteristics max.	Units
IGBT/Inverse Diode	Zero gate voltage collector current	I_{CES}	V_{CE} = 1200V V_{GE} = 0V		-	-	8.0	mA
	Gate-Emitter leakage current	I_{GES}	V_{CE} = 0V V_{GE}=±20V		-	-	1600	nA
	Gate-Emitter threshold voltage	$V_{GE(th)}$	V_{CE} = 20V I_C = 900mA		6.0	6.5	7.0	V
	Collector-Emitter saturation voltage	$V_{CE(sat)}$ (terminal) (*4)	I_C = 900A V_{GE}=15V	T_j= 25°C	-	1.75	2.20	V
				T_j=125°C	-	2.10	-	
				T_j=150°C		2.15	-	
		$V_{CE(sat)}$ (chip)		T_j= 25°C	-	1.65	2.10	
				T_j=125°C	-	2.00	-	
				T_j=150°C		2.05	-	
	Internal gate resistance	$R_{G(int)}$	-			1.19	-	Ω
	Input capacitance	C_{ies}	V_{CE}=10V, V_{GE}=0V,f=1MHz		-	83	-	nF
	Turn-on time	t_{on}	V_{CC} = 600V I_C = 900A V_{GE} = ±15V R_G = 1.6 Ω L_S = 70nH		-	1100	-	nsec
		t_r			-	500	-	
		$t_{r(i)}$			-	150	-	
	Turn-off time	t_{off}			-	1200	-	
		t_f			-	150	-	
	Forward on voltage	V_F (terminal) (*4)	I_F = 120A V_{GE}=0V	T_j= 25°C	-	1.70	2.15	V
				T_j=125°C	-	1.80	-	
				T_j=150°C		1.75	-	
		V_F (chip)		T_j= 25°C	-	1.65	2.10	
				T_j=125°C	-	1.75	-	
				T_j=150°C		1.70	-	
FWD	Reverse Current	I_R	V_{CE} = 1200V		-	-	8.0	mA
	Forward on voltage	V_F (terminal) (*4)	I_F = 900A V_{GE}=0V	T_j= 25°C	-	1.70	2.15	V
				T_j=125°C	-	1.80	-	
				T_j=150°C		1.75	-	
		V_F (chip)		T_j= 25°C	-	1.60	2.05	
				T_j=125°C	-	1.70	-	
				T_j=150°C		1.65	-	
	Reverse recovery time	t_{rr}	I_F = 900A		-	200	-	nsec
Thermistor	Resistance	R	T = 25°C		-	5000	-	Ω
			T = 100°C		465	495	520	
	B value	B	T = 25/50°C		3305	3375	3450	K

Note *4: Please refer to page 8, there is definition of on-state voltage at terminal.

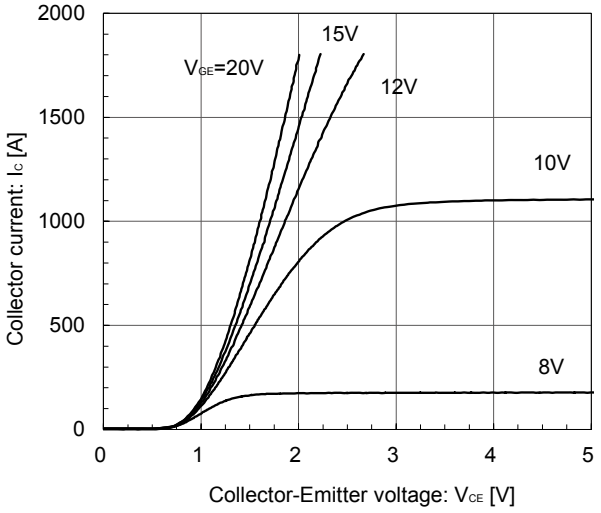
● Thermal resistance characteristics

Items	Symbols	Conditions	Characteristics min.	Characteristics typ.	Characteristics max.	Units
Thermal resistance (1device)	$R_{th(j-c)}$	Inverter IGBT	-	-	0.030	°C/W
		Inverse Diode	-	-	0.250	
		FWD	-	-	0.033	
Contact thermal resistance (1device) (*5)	$R_{th(c-f)}$	with Thermal Compound	-	0.00625	-	

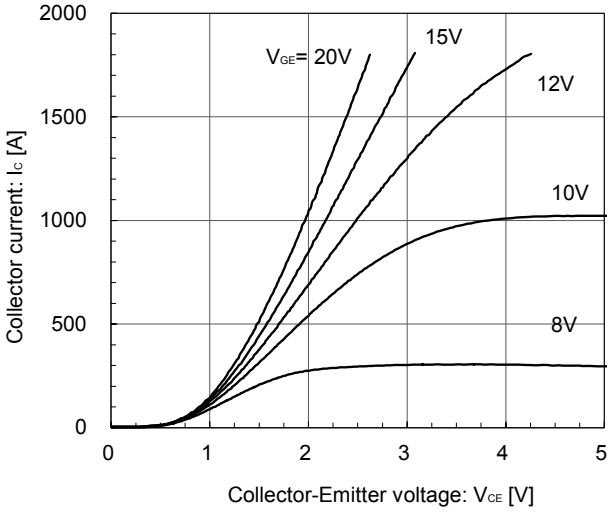
Note *5: This is the value which is defined mounting on the additional cooling fin with thermal compound.

■ Characteristics (Representative)

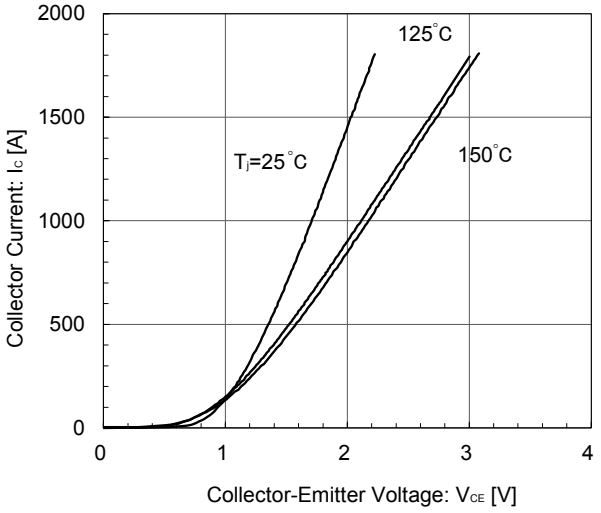
Collector current vs. Collector-Emitter voltage (typ.)
T_j= 25°C / chip



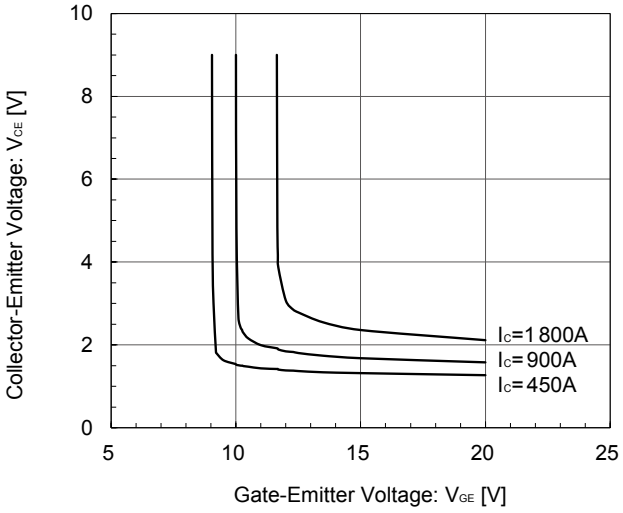
Collector current vs. Collector-Emitter voltage (typ.)
T_j= 150°C / chip



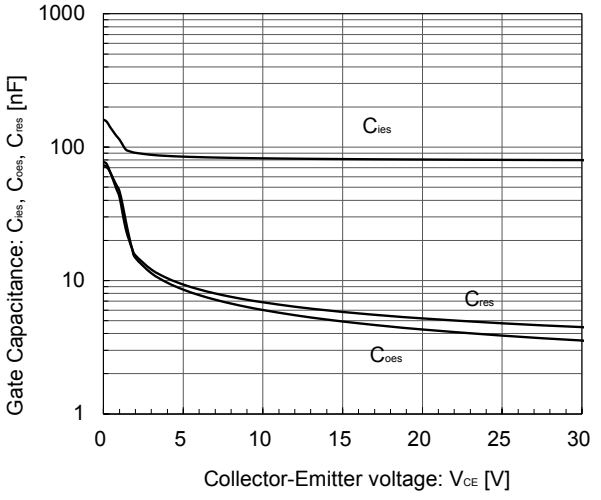
Collector current vs. Collector-Emitter voltage (typ.)
V_{GE}= 15V / chip



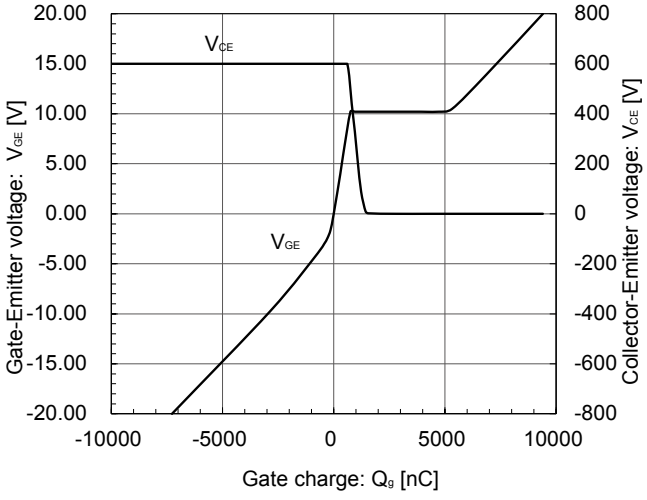
Collector-Emitter voltage vs. Gate-Emitter voltage (typ.)
T_j= 25°C / chip



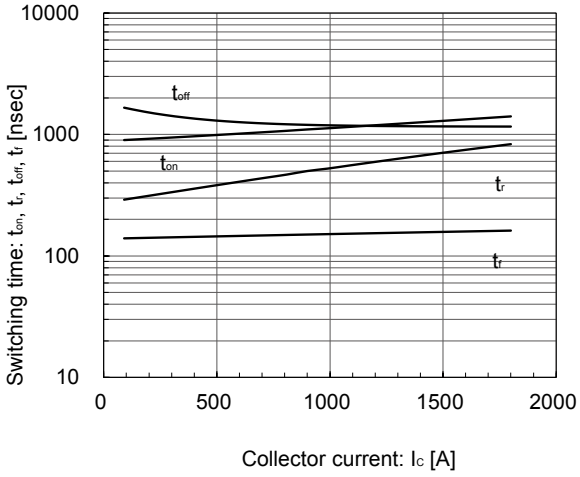
Gate Capacitance vs. Collector-Emitter Voltage (typ.)
V_{GE}= 0V, f= 1MHz, T_j= 25°C



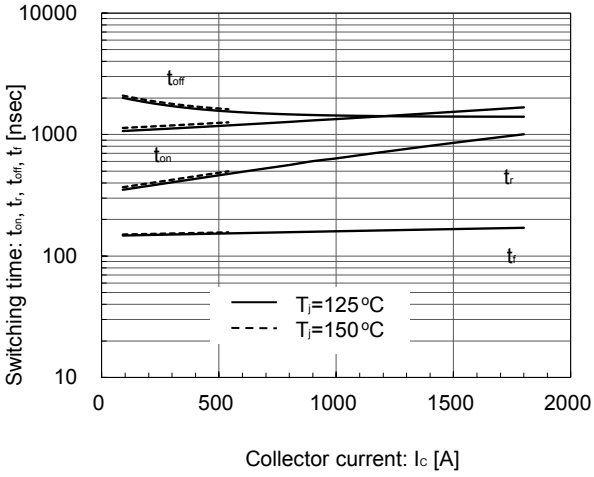
Dynamic Gate Charge (typ.)
V_{CC}=600V, I_C=900A, T_j= 25°C



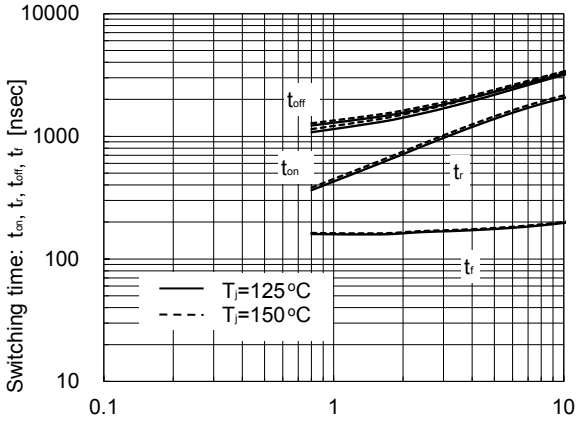
Switching time vs. Collector current (typ.)
V_{CC}=600V, V_{GE}=±15V, R_G=1.6Ω, T_j=25°C



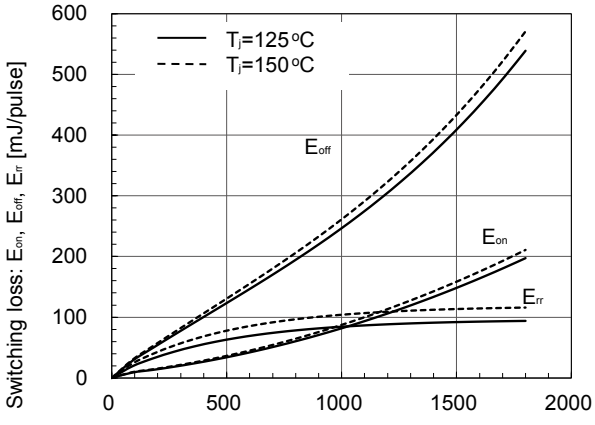
Switching time vs. Collector current (typ.)
V_{CC}=600V, V_{GE}=±15V, R_G=1.6Ω, T_j=125°C, 150°C



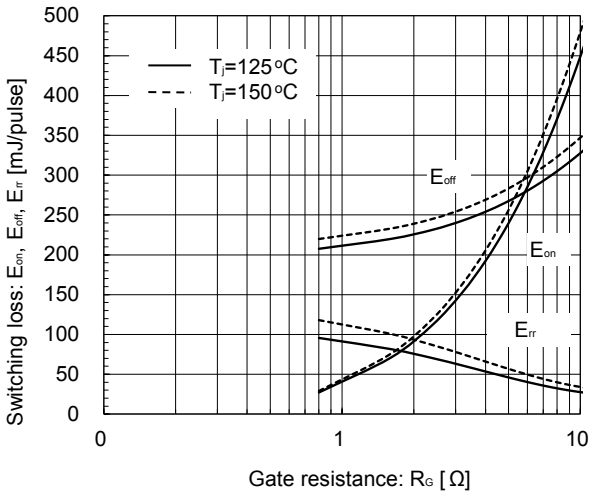
Switching time vs. Gate resistance (typ.)
V_{CC}=600V, I_C=900A, V_{GE}=±15V, T_j=125°C, 150°C



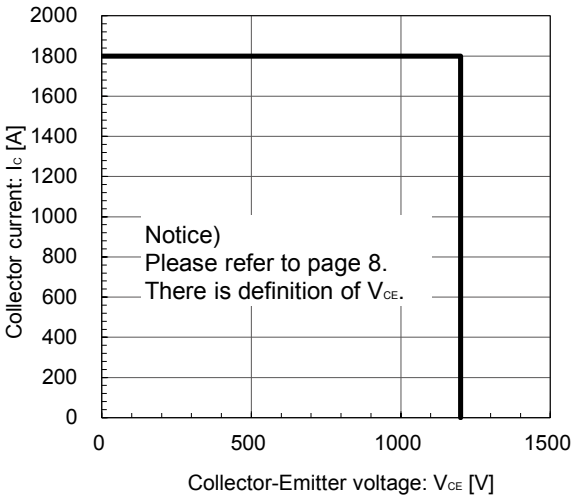
Switching loss vs. Collector current (typ.)
V_{CC}=600V, V_{GE}=±15V, R_G=1.6Ω, T_j=125°C, 150°C



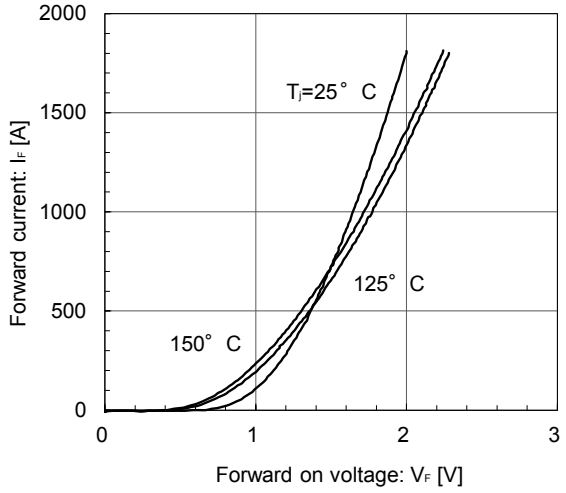
Switching loss vs. Gate resistance (typ.)
V_{CC}=600V, I_C=900A, V_{GE}=±15V, T_j=125°C, 150°C



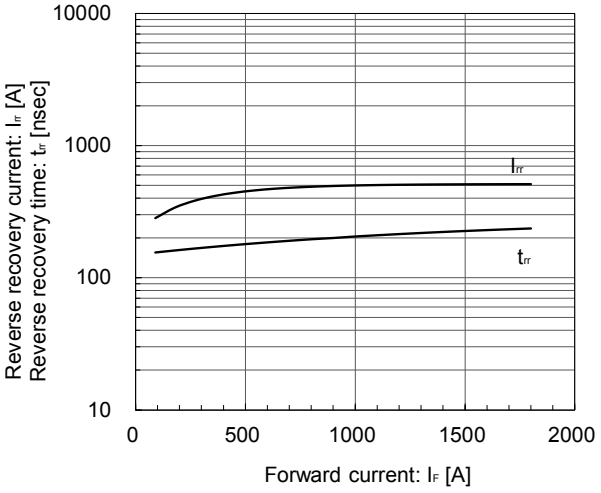
Reverse bias safe operating area (max.)
+V_{GE}=15V, -V_{GE}=15V, R_G=1.6Ω, T_j=150°C



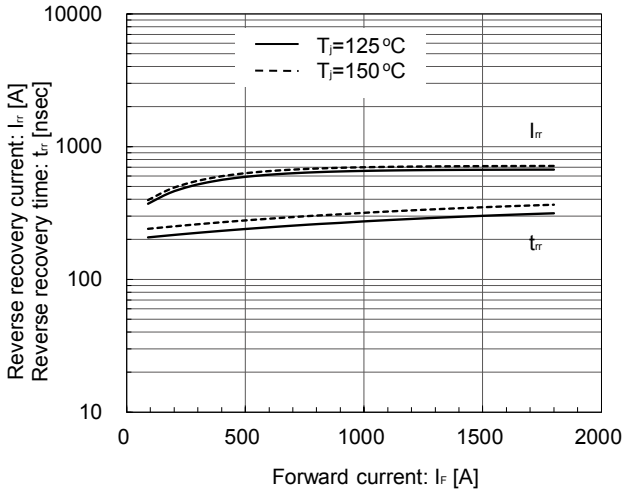
[FWD]
Forward Current vs. Forward Voltage (typ.)
chip



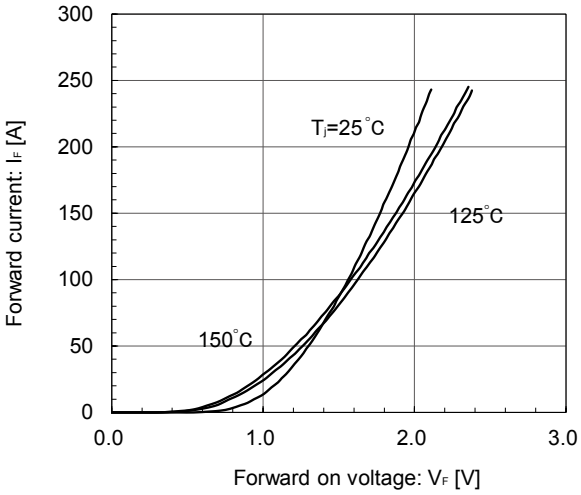
[FWD]
Reverse Recovery Characteristics (typ.)
V_{CC}=600V, V_{GE}=±15V, R_G=1.6Ω, T_j=25°C



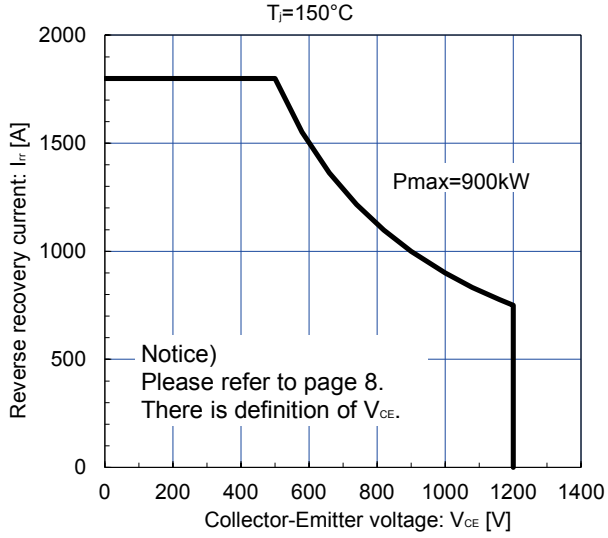
[FWD]
Reverse Recovery Characteristics (typ.)
V_{CC}=600V, V_{GE}=±15V, R_G=1.6Ω, T_j=125°C, 150°C



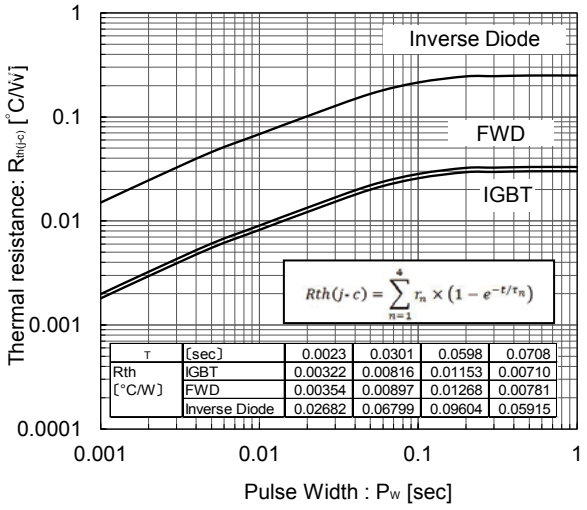
[Inverse Diode]
Forward Current vs. Forward Voltage (typ.)
chip



[FWD]
FWD safe operating area (max.)
T_j=150°C

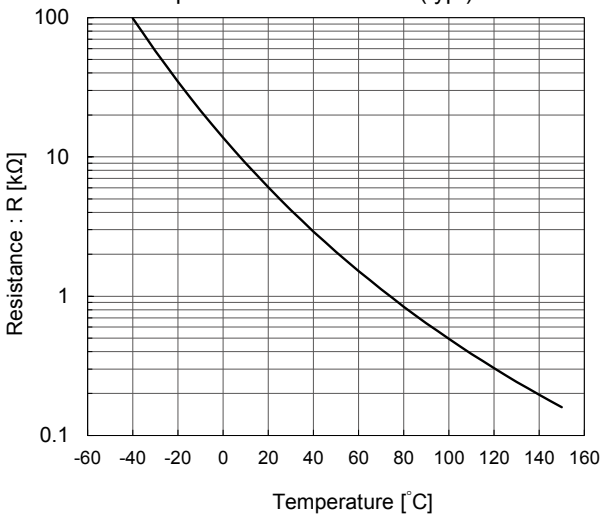


Transient Thermal Resistance (max.)

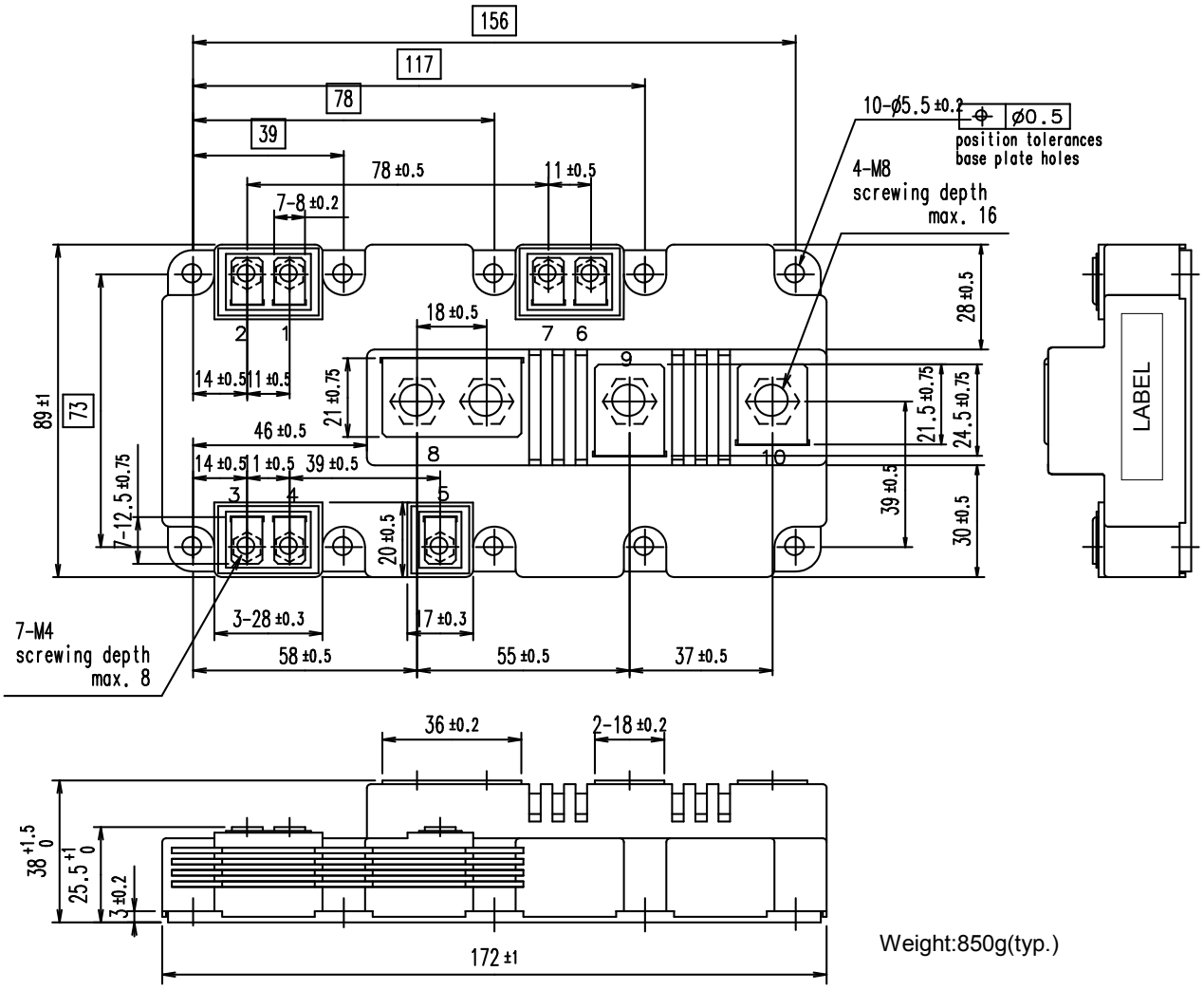


τ	[sec]	0.0023	0.0301	0.0598	0.0708
Rth [°C/W]	IGBT	0.00322	0.00816	0.01153	0.00710
	FWD	0.00354	0.00897	0.01268	0.00781
	Inverse Diode	0.02682	0.06799	0.09604	0.05915

[THERMISTOR]
Temperature characteristic (typ.)

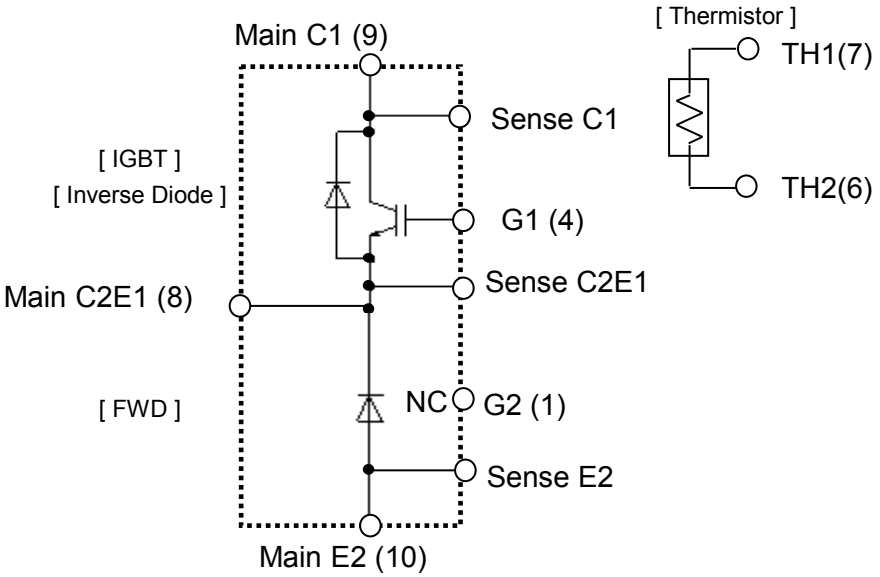


■ Outline Drawings, mm



Weight:850g(typ.)

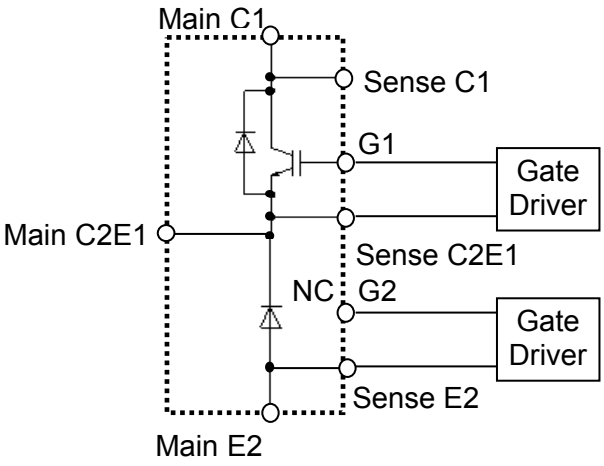
■ Equivalent Circuit Schematic



Notice) There is recommendation of wiring for NC terminal as follows

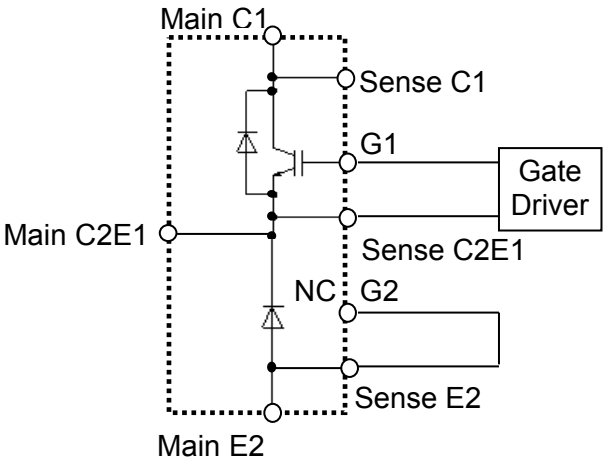
■ Fuji recommends wire connection of CASE1 or CASE2 to fix NC terminal voltage.

Case1



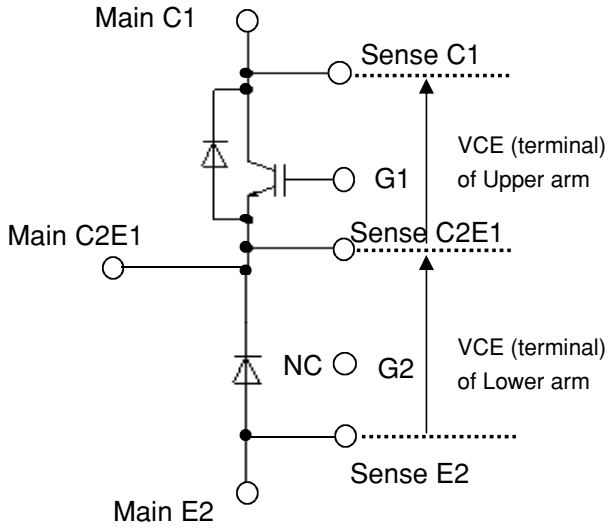
NC terminal (G2) and sense E2 should be connected by Gate-Driver.

Case2



NC terminal (G2) and sense E2 should be connected by wire.

■ Definition of on-state voltage at terminal and switching characteristics



Fuji defined VCE value of terminal by using Sense C1 and Sense C2E1 for Upper arm and Sense C2E1 and Sense E2 for Lower arm .

Switching characteristics of VCE also is defined between Sense C1 and Sense C2E1 for Upper arm and Sense C2E1 and Sense E2 for Lower arm .

Please use these terminals whenever measure spike voltage and on-state voltage .

WARNING

1. This Catalog contains the product specifications, characteristics, data, materials, and structures as of June 2015.
The contents are subject to change without notice for specification changes or other reasons. When using a product listed in this Catalog, be sur to obtain the latest specifications.

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• Machine tools • Audiovisual equipment • Electrical home appliances • Personal equipment • Industrial robots etc.

5. If you need to use a product in this Catalog for equipment requiring higher reliability than normal, such as for the equipment listed below, it is imperative to contact Fuji Electric Co., Ltd. to obtain prior approval. When using these products for such equipment, take adequate measures such as a backup system to prevent the equipment from malfunctioning even if a Fuji's product incorporated in the equipment becomes faulty.
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• Traffic-signal control equipment • Gas leakage detectors with an auto-shut-off feature
• Emergency equipment for responding to disasters and anti-burglary devices • Safety devices
• Medical equipment

6. Do not use products in this Catalog for the equipment requiring strict reliability such as the following and equivalents to strategic equipment (without limitation).
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• Submarine repeater equipment

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富士電機 Innovating Energy Technology

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IGBT Modules

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