

# 1MBI400V-120-50

**IGBT Modules** 

# IGBT MODULE (V series) 1200V / 400A / 1 in one package

#### Features

High speed switching Voltage drive Low Inductance module structure

#### ■ Applications

Inverter for Motor Drive AC and DC Servo Drive Amplifier Uninterruptible Power Supply Industrial machines, such as Welding machines



#### ■ Maximum Ratings and Characteristics

#### ● Absolute Maximum Ratings (at Tc=25°C unless otherwise specified)

Items		Symbols	Conditions		Maximum ratings		
Collector-Emitter voltage		Vces			1200	V	
Gate-Emitter voltage		V <sub>GES</sub>			±20	V	
		la .	Continuo	Tc=100°C	400		
Collector current		Ic	Continuous	Tc=25°C	480		
		Ic pulse	1ms		800	Α	
		-lc			400		
		-lc pulse	1ms		800		
Collector power dissipation		Pc	1 device		2410	W	
Junction temperature		Tj			175		
Operating junction temperature (under switching conditions)		Tjop		150		°C	
Case temperature		Tc			125		
Storage temperature		Tstg			-40~+125		
Isolation voltage	Between terminal and copper base (*1)	Viso	AC : 1min.		2500	VAC	
Screw torque	Mounting (*2)	M5 ro M6			6.0		
	Terminals (*3)	M4			2.0	Nm	
		M6			5.0		

Note \*1: All terminals should be connected together during the test.

Note \*2: Recommendable Value : 3.0-6.0 Nm (M5, M6) Note \*3: Recommendable Value : 1.1-2.0 Nm (M4) Recommendable Value : 2.5-5.0 Nm (M6)

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#### ● Electrical characteristics (at Tj= 25°C unless otherwise specified)

Home	Cumbala	Conditions		Characteristics			Huita
Items	Symbols			min.	typ.	max.	Units
Zero gate voltage collector current	Ices	V <sub>GE</sub> = 0V, V <sub>CE</sub> = 1200V	V <sub>GE</sub> = 0V, V <sub>CE</sub> = 1200V		-	2.0	mA
Gate-Emitter leakage current	Iges	V <sub>CE</sub> = 0V, V <sub>GE</sub> = ±20V	$V_{CE} = 0V, V_{GE} = \pm 20V$		-	800	nA
Gate-Emitter threshold voltage	V <sub>GE (th)</sub>	V <sub>CE</sub> = 20V, I <sub>C</sub> = 400mA		6.0	6.5	7.0	V
			Tj=25°C	-	1.95	2.40	V
	(terminal)	$V_{GE} = 15V$ $I_{C} = 400A$	Tj=125°C	-	2.25	-	
<b>.</b>			Tj=150°C		2.30		
Collector-Emitter saturation voltage			Tj=25°C	-	1.75	2.15	
	V <sub>CE</sub> (sat)		Tj=125°C	-	2.05	-	
	(chip)		Ti=150°C		2.10		
Input capacitance	Cies	V <sub>GE</sub> = 0V, V <sub>CE</sub> = 10V, f = 1MHz		-	36	-	nF
· ·	ton			-	0.60	-	
Turn-on time	tr	Vcc = 600V, Ic = 400A	-	0.20	-	μs	
	tr(i)	$V_{GE} = \pm 15V$ , $R_G = 1.80$	-	0.08	-		
	toff	Tj=150°C, Ls=35nH		-	1.00		-
Turn-off time	tf			-	0.14		-
	VF		Tj=25°C	-	1.85	2.30	V
	(terminal)	V <sub>GF</sub> = 0V	Tj=125°C	-	2.00	-	
	,		Tj=150°C		1.95		
Forward on voltage	VF	I <sub>F</sub> = 400A		-	1.70	2.15	
	(chip)	)	Tj=125°C	-	1.85	-	
	. ,		Tj=150°C		1.80		
Reverse recovery time	trr	I <sub>F</sub> = 400A		-	0.20	-	us

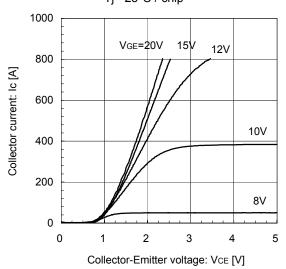
#### ● Thermal resistance characteristics

Items	Symbols	Conditions	Characteristics			Units
items		Conditions	min.	typ.	max.	Uiiits
Thermal vaciation on (4 device)	Rth(j-c)	IGBT	-	-	0.062	°C/W
Thermal resistance (1device)		FWD	-	-	0.110	
Contact thermal resistance (*4)	t thermal resistance (*4) Rth(c-f)		-	0.0125	-	

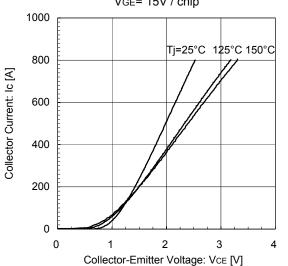
Note \*4: This is the value which is defined mounting on the additional cooling fin with thermal compound.

#### **■** Characteristics (Representative)

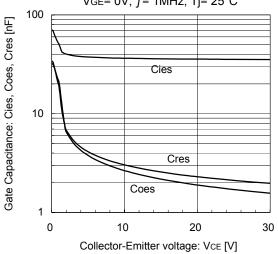
Collector current vs. Collector-Emitter voltage (typ.) Tj= 25°C / chip



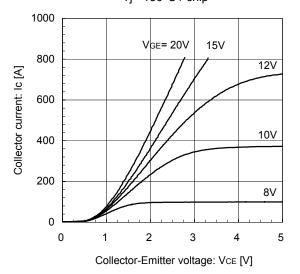
Collector current vs. Collector-Emitter voltage (typ.) VGE= 15V / chip



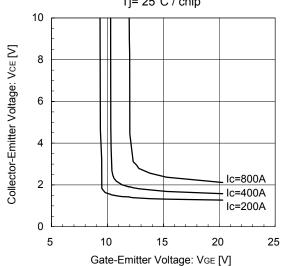
Gate Capacitance vs. Collector-Emitter Voltage VGE= 0V, *f* = 1MHz, Tj= 25°C



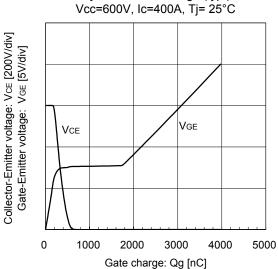
Collector current vs. Collector-Emitter voltage (typ.) Tj= 150°C / chip



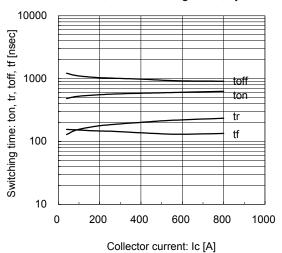
Collector-Emitter voltage vs. Gate-Emitter voltage Tj= 25°C / chip



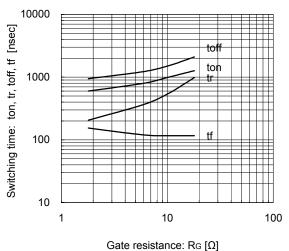
Dynamic Gate Charge (typ.)



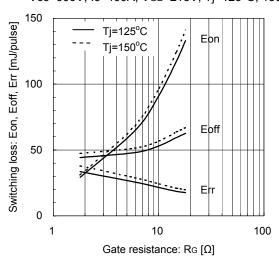
Switching time vs. Collector current (typ.) Vcc=600V,  $VgE=\pm15V$ ,  $Rg=1.8\Omega$ ,  $Tj=125^{\circ}C$ 



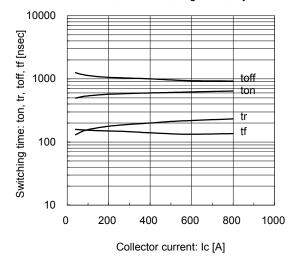
Switching time vs. Gate resistance (typ.) Vcc=600V, Ic=400A,  $VgE=\pm15V$ ,  $Tj=125^{\circ}C$ 



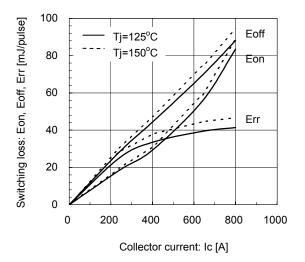
Switching loss vs. Gate resistance (typ.) Vcc=600V, Ic=400A, VgE=±15V, Tj=125°C, 150°C



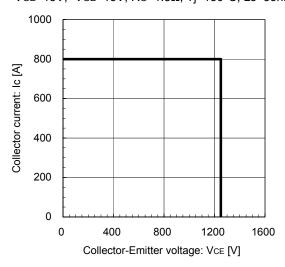
Switching time vs. Collector current (typ.) Vcc=600V,  $VgE=\pm15V$ ,  $Rg=1.8\Omega$ ,  $Tj=150^{\circ}C$ 



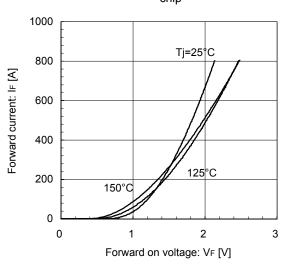
Switching loss vs. Collector current (typ.) Vcc=600V, VgE= $\pm$ 15V, Rg=1.8 $\Omega$ , Tj=125°C, 150°C



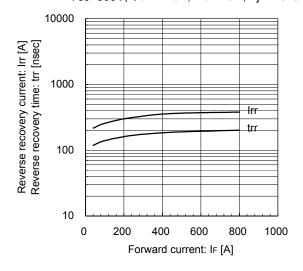
Reverse bias safe operating area (max.) +VGE=15V, -VGE=15V, RG=1.8 $\Omega$ , Tj=150°C, Ls=35nH



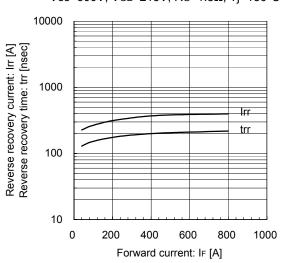
Forward Current vs. Forward Voltage (typ.) chip



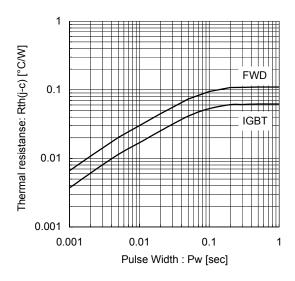
Reverse Recovery Characteristics (typ.) Vcc=600V, VgE= $\pm$ 15V, Rg=1.8 $\Omega$ , Tj=125°C



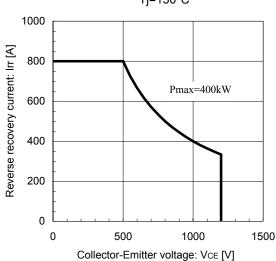
Reverse Recovery Characteristics (typ.) Vcc=600V, VgE=±15V, Rg=1.8Ω, Tj=150°C



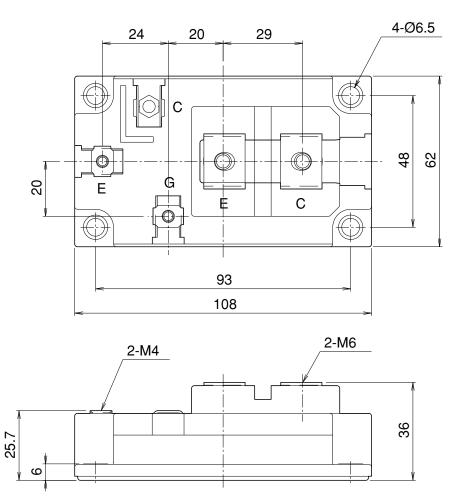
Transient Thermal Resistance (max.)



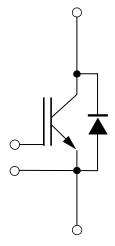
FWD safe operating area (max.) Tj=150°C



## Outline Drawings, mm



## **■** Equivalent Circuit Schematic



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