

#### SKM150GAR12V

#### **Features**

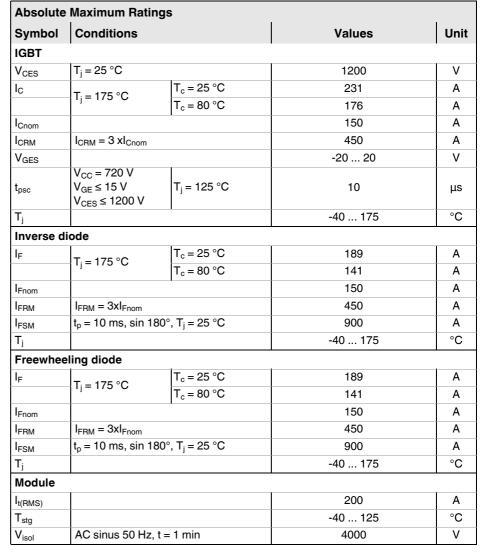
- V-IGBT = 6. Generation Trench V-IGBT (Fuji)
- CAL4 = Soft switching 4. Generation CAL-diode
- Insulated copper baseplate using DBC technology (Direct Copper Bonding)
- · Increased power cycling capability
- · With integrated gate resistor
- UL recognized, file no. E63532
- Lowest switching losses at High di/dt

#### Typical Applications\*

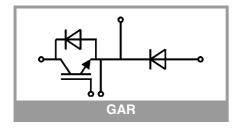
- Electronic welders
- DC/DC converter
- · Brake chopper
- · Switched reluctance motor

#### **Remarks**

- Case temperature limited to T<sub>c</sub> = 125°C
- Recommended  $T_{op} = -40 \dots +150$ °C
- Product reliability results valid for T<sub>j</sub> = 150°C



Characteristics										
Symbol	Conditions	min.	typ.	max.	Unit					
IGBT	·									
V <sub>CE(sat)</sub>	$I_C = 150 \text{ A}$ $V_{GE} = 15 \text{ V}$ chiplevel	T <sub>j</sub> = 25 °C		1.75	2.20	V				
		T <sub>j</sub> = 150 °C		2.20	2.48	V				
V <sub>CE0</sub>	chiplevel	T <sub>j</sub> = 25 °C		0.94	1.04	V				
		T <sub>j</sub> = 150 °C		0.88	0.98	V				
r <sub>CE</sub>	V <sub>GE</sub> = 15 V chiplevel	T <sub>j</sub> = 25 °C		5.4	7.7	mΩ				
		T <sub>j</sub> = 150 °C		8.8	10	mΩ				
V <sub>GE(th)</sub>	$V_{GE}=V_{CE}$ , $I_{C}=6$ mA		5.5	6	6.5	V				
I <sub>CES</sub>	V <sub>GE</sub> = 0 V V <sub>CE</sub> = 1200 V	T <sub>j</sub> = 25 °C			0.3	mA				
		T <sub>j</sub> = 150 °C		-		mA				
C <sub>ies</sub>	V <sub>CE</sub> = 25 V V <sub>GE</sub> = 0 V	f = 1 MHz		9.0		nF				
C <sub>oes</sub>		f = 1 MHz		0.89		nF				
C <sub>res</sub>		f = 1 MHz		0.88		nF				
$Q_{G}$	V <sub>GE</sub> = - 8 V+ 15 V			1650		nC				
R <sub>Gint</sub>	T <sub>j</sub> = 25 °C			5.0		Ω				





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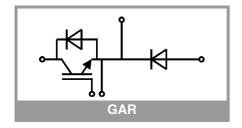
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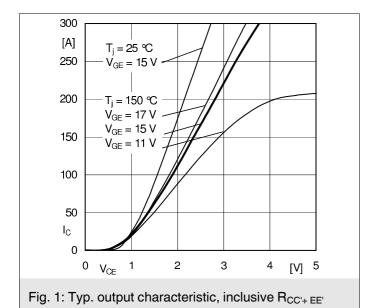
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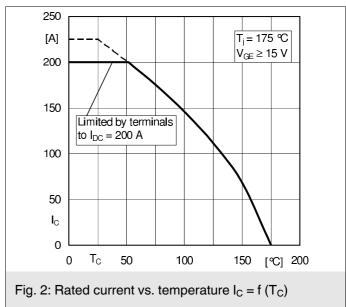
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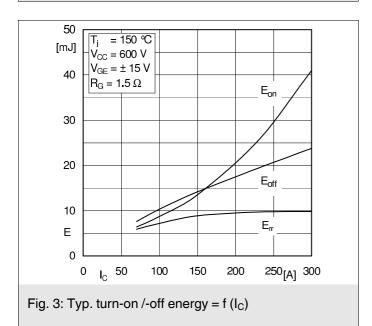
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- Recommended  $T_{op} = -40 \dots +150$ °C
- Product reliability results valid for T<sub>i</sub> = 150°C

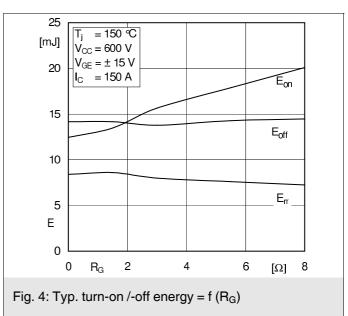
Characteristics									
Symbol	Conditions	min.	typ.	max.	Unit				
t <sub>d(on)</sub>	V <sub>CC</sub> = 600 V	T <sub>i</sub> = 150 °C		258		ns			
t <sub>r</sub>	I <sub>C</sub> = 150 A	T <sub>i</sub> = 150 °C		32		ns			
E <sub>on</sub>	$V_{GE} = +15/-15 \text{ V}$ $R_{G \text{ on}} = 1.5 \Omega$	T <sub>j</sub> = 150 °C		13.5		mJ			
t <sub>d(off)</sub>	$R_{G \text{ off}} = 1.5 \Omega$	T <sub>j</sub> = 150 °C		388		ns			
t <sub>f</sub>	$di/dt_{on} = 5400 \text{ A/}\mu\text{s}$	T <sub>j</sub> = 150 °C		62		ns			
E <sub>off</sub>	di/dt <sub>off</sub> = 1800 A/μs du/dt = 8100 V/μs	T <sub>j</sub> = 150 °C		14.2		mJ			
R <sub>th(j-c)</sub>	per IGBT	I			0.19	K/W			
Inverse di	iode								
11 120	$I_F = 150 \text{ A}$ $V_{GE} = 0 \text{ V}$ chiplevel	T <sub>j</sub> = 25 °C		2.14	2.46	V			
		T <sub>j</sub> = 150 °C		2.07	2.38	V			
V <sub>F0</sub>	chiplevel	T <sub>j</sub> = 25 °C		1.30	1.50	V			
		T <sub>j</sub> = 150 °C		0.90	1.10	V			
r <sub>F</sub>	chiplevel	T <sub>j</sub> = 25 °C		5.6	6.4	$m\Omega$			
	Chipievei	T <sub>j</sub> = 150 °C		7.8	8.5	$m\Omega$			
I <sub>RRM</sub>	I <sub>F</sub> = 150 A	T <sub>j</sub> = 150 °C		165		Α			
$Q_{rr}$	$di/dt_{off} = 5800 \text{ A/}\mu\text{s}$ $V_{GE} = \pm 15 \text{ V}$	T <sub>j</sub> = 150 °C		22		μC			
E <sub>rr</sub>	$V_{CC} = 600 \text{ V}$	T <sub>j</sub> = 150 °C		8.5		mJ			
R <sub>th(j-c)</sub>	per diode				0.31	K/W			
Freewhee	ling diode								
$V_F = V_{EC}$	I <sub>F</sub> = 150 A	T <sub>j</sub> = 25 °C		2.14	2.46	V			
V <sub>GE</sub> = 0 V chiplevel		T <sub>j</sub> = 150 °C		2.07	2.38	V			
V <sub>F0</sub>	chiplevel	T <sub>j</sub> = 25 °C		1.30	1.50	V			
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R <sub>th(j-c)</sub>	per diode				0.31	K/W			
Module									
L <sub>CE</sub>				30		nH			
R <sub>CC'+EE'</sub>	measured per switch	T <sub>C</sub> = 25 °C		0.65		mΩ			
		T <sub>C</sub> = 125 °C		1.09		mΩ			
R <sub>th(c-s)</sub>	calculated without thermal coupling (λ <sub>grease</sub> =0.81 W/(m*K))			0.04	0.05	K/W			
Ms	to heat sink M6		3		5	Nm			
Mt		to terminals M5	2.5		5	Nm			
						Nm			
w					160	g			

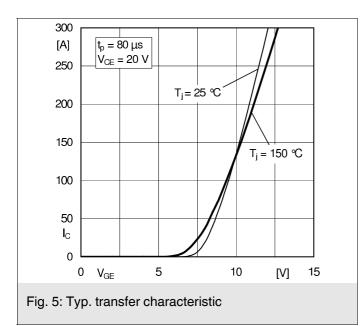


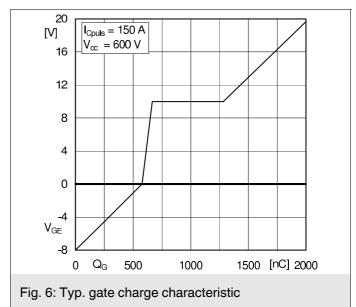


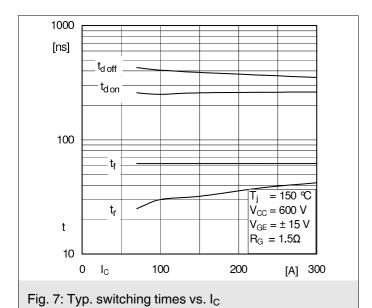


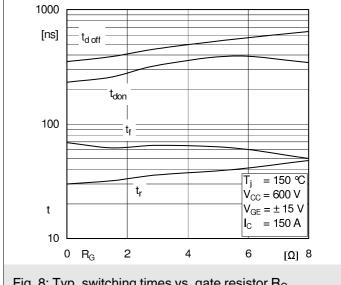


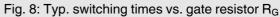












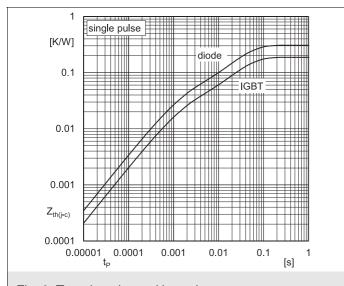


Fig. 9: Transient thermal impedance

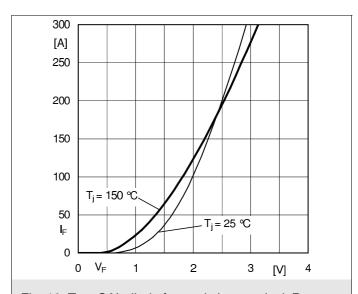


Fig. 10: Typ. CAL diode forward charact., incl. R<sub>CC'+ EE'</sub>

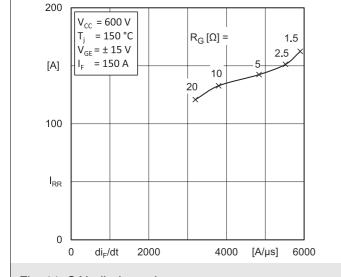


Fig. 11: CAL diode peak reverse recovery current

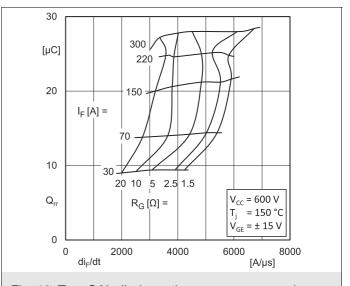
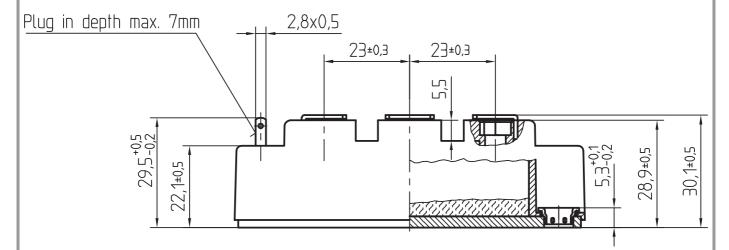
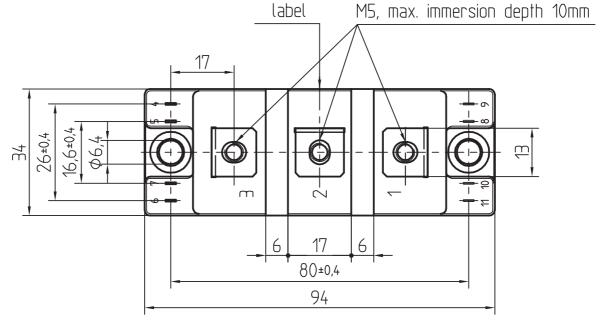


Fig. 12: Typ. CAL diode peak reverse recovery charge

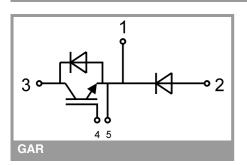






General tolerance +/- 0,5 mm

### SEMITRANS 2



### SKM<u>150GAR12V</u>

This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

#### \*IMPORTANT INFORMATION AND WARNINGS

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